

Professional high-speed optical transceiver

OPN-SR15-80LC2-HM

3.3V / 1550 nm / 2.5 Gbps Digital Diagnostic SFP LC SINGLE-MODE TRANSCEIVER

PRODUCT FEATURES

- Hot-Pluggable SFP Footprint LC Optical Transceiver
- Small Form-Factor Pluggable (SFP) MSA compatible
- Compliant with SONET OC-48 (LR-2) / SDH STM-16(L-16.2)
 - Compliant with Fibre Channel 1x/2x SM-LC-L FC-PI
 - Compliant with IEEE 802.3z 1000BASE
 - SFF-8472 Digital Diagnostic Function
 - 1550 nm DFB LD Transmitter
 - APD High Sensitivity Receiver
 - Link Budget at 1551 nm: 14-28 dB
 - AC/AC Coupling according to MSA
 - Single +3.3 V Power Supply
 - RoHS Compliant
 - 0 to 70°C Operation : OPN-SR15-80LC2-HM
- Class 1 Laser International Safety Standard IEC-60825Compliant

APPLICATIONS

- ATM Switches and Routers
- SONET / SDH Switch Infrastructure
 - XDSL Applications
 - Metro Edge Switching

PRODUCT DESCRIPTION

The OPN-SR15-80LC2-HM series single mode transceivers is small form factor pluggable module for bi-directional serial optical data communications such as SONET OC-48 / SDHSTM-16, Gigabit Ethernet 1000BASE-LX and FibreChannel 1x/2x SM-LC-L FC-PI. It is with the SFP 20-pin connector to allow hot plug capability. Digital diagnostic functions are available via an I²C. This module is designedfor single mode fiber and operates at a nominal wavelengthof 1550 nm. A guaranteed minimum optical link budget of 14-28 dB is offered which can correspond to a link distance ofover 80 km (assuming worst case fiber loss of 0.25 dB/km).The transmitter section uses a multiple quantum well 1550 nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. Thereceiver section uses an integrated InGaAs detectorpreamplifier (IDP) mounted in an optical header and alimiting post-amplifier IC.

ORDER INFORMATION

P/No.	Bit Rate (Gb/s)	SONET /SDH	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
OPN-SR15-80LC2-HM	2.488	LR-2/L-16.2	80	1550 DFB	LC SFP with DMI	0 to 70	3 to -2	-28	Yes

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-50	85	°C	
Operating Case Temperature	Topr	0	70	°C	OPN-SR15-80LC2-HM
Power Supply Voltage	Vcc	-0.5	3.6	V	

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Recommended Operating Conditions					
Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	V _{CC}	3.13	3.3	3.47	V
Operating Case Temperature	T _{opr}	0		70	°C / OPN-SR15-80LC2-HM
Power Supply Current	I _{CC} (TX+RX)		230	300	mA
Data Rate		622	2488	2670	Mb/s / 1
Fiber Type			SM 9/125 μm		Single-Mode
Link Budget @ 1550 nm		14		28	dB
Attenuation	A		0.25		dB/km
Dispersion	D		19		ps/(nm·km)

1. OC-48, Gigabit Ethernet and 1x/2x Fibre Channel compliant.

Transmitter Specifications (0°C < T _{opr} < 70°C, 3.13V < V _{CC} < 3.47V)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Optical						
Optical Transmit Power	P _o	-2	---	3	dBm	1
Output Center Wavelength	λ	1500	1550	1580	nm	
Output Spectrum Width	Δλ	---	---	1	nm	-20 dB Width
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	E _R	8.2	---	---	dB	
Output Eye	Compliant with Telecordia GR-253-GORE and ITU-T Recommendation G.957					
Optical Rise Time	t _r			150	ps	20 % to 80% Values
Optical Fall Time	t _f			150	ps	20 % to 80% Values
Relative Intensity Noise	RIN			-120	dB/Hz	
Dispersion Penalty				2	dB	2
Electrical						
Data Input Current – Low	I _{IL}	-350			μA	
Data Input Current – High	I _{IH}			350	μA	
Differential Input Voltage	V _{IH} - V _{IL}	0.5		2.4	V	Peak-to-Peak
TX Disable Input Voltage – Low	T _{DIS, L}	0		0.5	V	3
TX Disable Input Voltage – High	T _{DIS, H}	2.0		V _{CC}	V	3
TX Disable Assert Time	T _{ASSERT}			10	μs	
TX Disable Deassert Time	T _{DEASSERT}			1	ms	
TX Fault Output Voltage -- Low	T _{FaultL}	0		0.5	V	4
TX Fault Output Voltage -- High	T _{FaultH}	2.0		V _{CC} +0.3	V	4

1. Output power is power coupled into a 9/125 μm single mode fiber.
2. Specified at 1600 ps/nm dispersion over G.652/G.654 fiber with center wavelength range of 1500 to 1580 nm.
3. There is an internal 4.7K to 10K ohm pull-up resistor to V_{CC}TX.
4. Open collector compatible, 4.7K to 10K ohm pull-up to V_{CC} (Host Supply Voltage).

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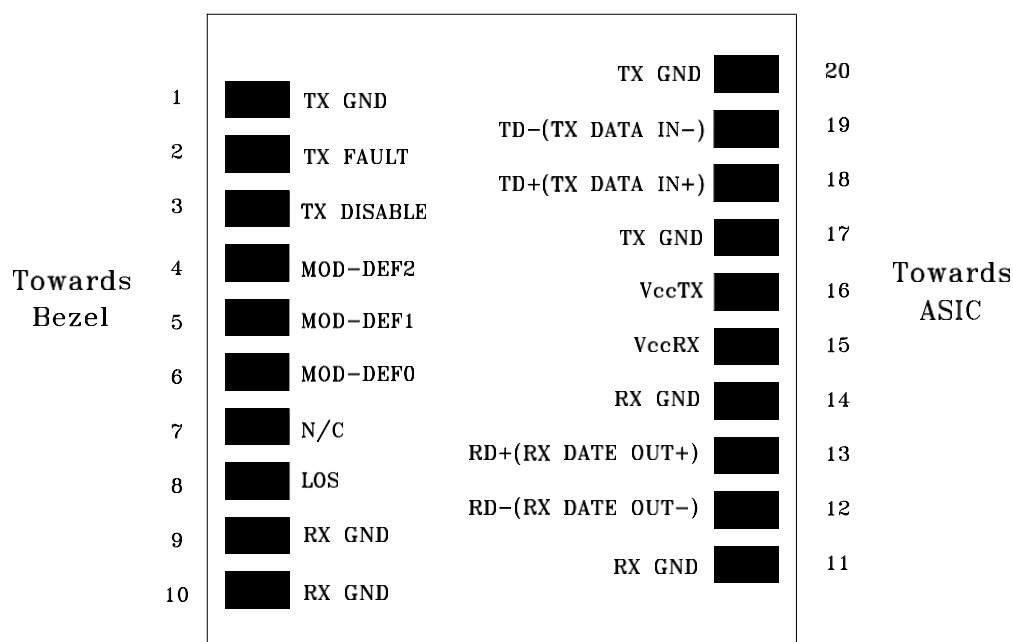
Receiver Specifications (0°C < Topr < 70°C, 3.13V < Vcc < 3.47V)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Optical						
Sensitivity	Sens			-28	dBm	5,6
Maximum Input Power	Pin	-9			dBm	5,6
Signal Detect -- Asserted	Pa	---		-28	dBm	Transition: low to high
Signal Detect -- Deasserted	Pd	-50	---	---	dBm	Transition: high to low
Signal detect -- Hysteresis		1.0	---		dB	
Wavelength of Operation		1100	---	1600	nm	
Electrical						
Differential Output Voltage	$V_{OH} - V_{OL}$	0.6		2.0	V	
Output LOS Voltage -- Low	V_{OL}	0		0.5	V	7
Output LOS Voltage -- High	V_{OH}	2.0		$V_{cc}+0.3$	V	7

5. Measured at 2²³-1 PRBS at BER 1E-10.

6. Measured at 2⁷-1 PRBS at BER 1E-12 for 2.125 Gb/s, 1.25Gb/s, and 1.063 Gb/s.

7. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

CONNECTION DIAGRAM



PIN	Signal Name	Description	PIN	Signal Name	Description
1	TX GND	Transmitter Ground	11	RX GND	Receiver Ground
2	TX Fault	Transmitter Fault Indication	12	RX DATA OUT-	Inverse Receiver Data Out
3	TX Disable	Transmitter Disable (Module disables on high or open)	13	RX DATA OUT+	Receiver Data Out
4	MOD-DFE2	Modulation Definition 2 – Two wires serial ID Interface	14	RX GND	Receiver Ground
5	MOD-DEF1	Modulation Definition 1 – Two wires serial ID Interface	15	Vcc RX	Receiver Power – 3.3V±5%

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6	MOD-DEF0	Modulation Definition 0 – Ground in Module	16	Vcc TX	Transmitter Power – 3.3V±5%
7	N/C	Not Connected	17	TX GND	Transmitter Ground
8	LOS	Loss of Signal	18	TX DATA IN+	Transmitter Data In
9	RX GND	Receiver Ground	19	TX DATA IN-	Inverse Transmitter Data In
10	RX GND	Receiver Ground	20	TX GND	Transmitter Ground

Module Definition

Module Definition	MOD-DEF2 PIN 4	MOD-DEF1 PIN 5	MOD-DEF0 PIN 6	Interpretation by Host
4	SDA	SCL	LV-TTL Low	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E²PROM protocol of the ATMEL AT24C01A/02/04 family of components.

RECOMMENDED CIRCUIT SCHEMATIC

